

09/787353

532 PCT/PTO 16 MAR 2001

Patent
Attorney's Docket No. 032658-016

#5/A
aA
10/2/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Richard Phelps et al.

Application No.: Unassigned

Filed: March 16, 2001

For: APPARATUS FOR USE IN A COMPUTER
SYSTEM

)
)
) Group Art Unit: Unassigned

)
) Examiner: Unassigned
)
)
)
)
)
)

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified application as follows:

IN THE CLAIMS:

Please replace claims ~~4-10~~, ~~11-14~~, ~~16~~, ~~18-20~~, ~~22-24~~, ~~26-27~~, ~~32-33~~ and ~~35-38~~ as follows.

4. (Amended) Apparatus as claimed in claim 1, wherein, latency tolerant modules connected to the secondary bus are arranged such that the least tolerant modules are located closer to the primary bus than the more tolerant modules.

5. (Amended) Apparatus as claimed in claim 1, wherein the bus architecture has two or more secondary buses, each one connected to the primary bus via a separate primary to secondary interface module.

6. (Amended) Apparatus as claimed in claim 1, wherein the primary bus comprises:
a write data bus for transferring write data between modules;
a read data bus for transferring read data between modules;
a transaction bus for transferring control data between modules,
the read data bus and the write data bus being physically separate from one another.